

**AMENDMENTS TO THE SPECIFICATION**

**Please rewrite the paragraph bridging page 14, line 19, through page 15, line 7, of the specification as follows:**

As shown in Figs. 1 and 2, the CCD can selectively output only the image data read ~~by either~~ by either the odd or even photodiode arrays. And then, even if the frequencies of the transfer clocks  $\phi 1$  and  $\phi 2$  and reset clock RS are increased twice those in the prior art, sufficient output time can be ensured for image data in each element which is outputted from the output buffer. More specifically, as compared with the case shown in Fig. 21 in which image in all the elements are outputted from the CCD and thinned out by the S/H circuit, with the operation shown in Figs. 1 and 2, the interval between the instant at which image data is outputted from the shift register to the output buffer and the instant at which the image data is reset is long, and hence the precision of outputting data from the CCD (output buffer) improves.